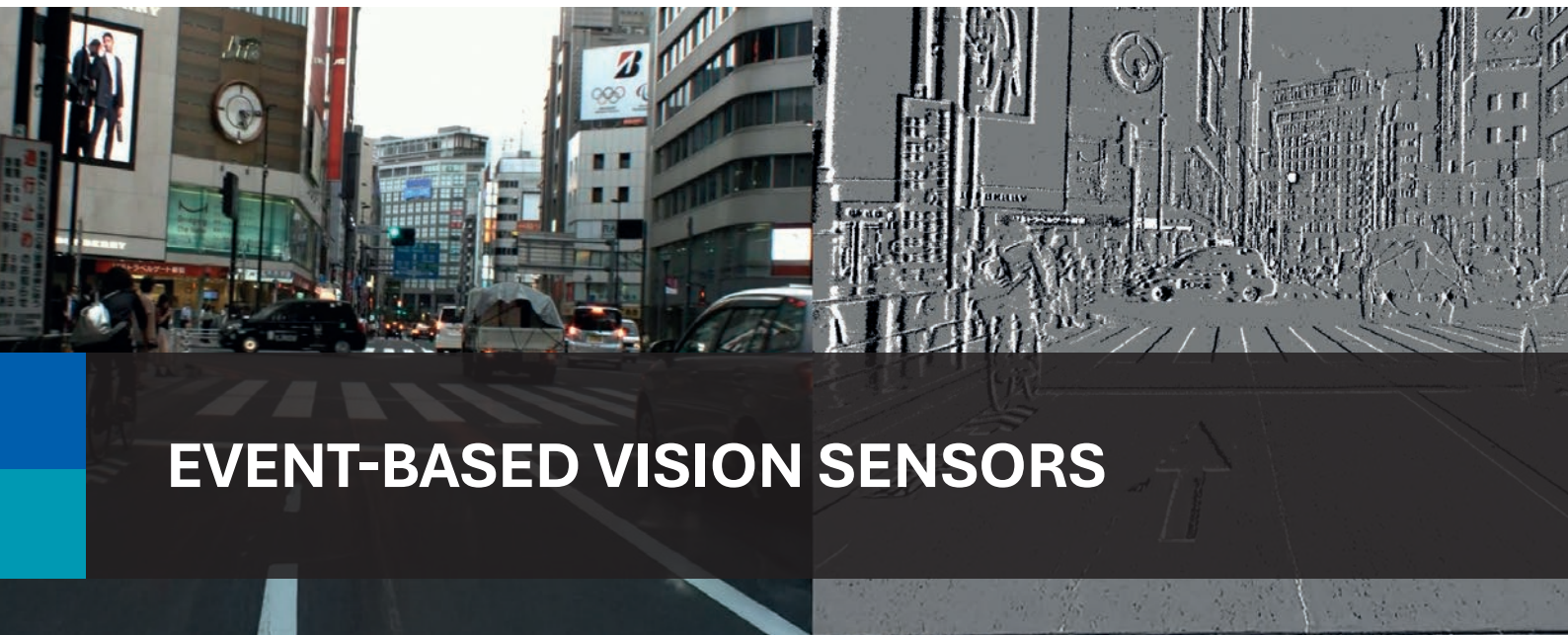




Model name		IMX636	IMX637	IMX646	IMX647
Effective pixels		1280(H)x720(V) Approx. 0.92 megapixels	640(H)x512(V) Approx. 0.32 megapixels	1280(H)x720(V) Approx. 0.92 megapixels	640(H)x512(V) Approx. 0.32 megapixels
Unit cell size		4.86µm x 4.86µm		4.86µm x 4.86µm	
Image size		Diagonal 7.137mm (1/2.5-type)	Diagonal 3.983mm (1/4.5-type)	Diagonal 7.137mm (1/2.5-type)	Diagonal 3.983mm (1/4.5-type)
Color filter		Black and white		Black and white	
Package		Ceramic LGA Package (Exterior: 13 x 13 mm)		Ceramic LGA Package (Exterior: 13 x 13 mm)	
Power supply	Analog	3.0V		3.0V	
	Digital	1.1V		1.1V	
	Interface	1.8V		1.8V	
Nominal contrast threshold (ln)		25%		25%	
Latency	ROI*2	Less than 100 microseconds @ 1 Klux Less than 1000 microseconds @ 5 lux		Less than 800 microseconds @ 1 Klux Less than 9000 microseconds @ 0.3 lux	
	Thinned read-out*3	Less than 220 microseconds @ 1 Klux Less than 1000 microseconds @ 5 lux		Less than 800 microseconds @ 1 Klux Less than 9000 microseconds @ 0.3 lux	
Maximum event rate		1.06 Geps (giga events per second)		1.06 Geps (giga events per second)	
Dynamic range		86 dB or more (5-100,000 lux) *4		110 dB or more (0.3-100,000 lux) *4	
Background rate		0.1Hz@1klux/10Hz@5lux		0.1Hz@1klux/5Hz@0.3lux	
Interface		Input: I2C (400 kHz / 1 Mhz), 4-wire SPI		Input: I2C (400 kHz / 1 Mhz), 4-wire SPI	
		Output: MIPI D-PHY (1.5 Gbps/lane) 2lane, SLVS (800 Mbps/lane) 4lane		Output: MIPI D-PHY (1.5 Gbps/lane) 2lane, SLVS (800 Mbps/lane) 4lane	
Event signal processing function		Anti-flicker/ Event filter/ Event rate control		Anti-flicker/ Event filter/ Event rate control	